

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number	09989765
	Filing Date	2001-11-19
	First Named Inventor	Manfred Bartz
	Art Unit	2176
	Examiner Name	Maikhanh Nguyen
	Attorney Docket Number	CD01194M

U.S.PATENTS						
Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	6910126	B1	2005-06-21	Mar et al.	Entire Document
	2	7283151	B1	2007-10-16	Nihei et al.	Entire Document
	3	6798299	B1	2004-09-28	Mar et al.	Entire Document
	4	7023257	B1	2006-04-04	Suliam	Entire Document
	5	6967511	B1	2005-11-22	Sullam	Entire Document
	6	6892322	B1	2005-05-10	Snyder	Entire Document
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9	6611220	B1	2003-08-26	Snyder	Entire Document
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12	7386740	B1	2008-06-10	Kutz et al.	Entire Document
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44	5745011	B1	1998-04-28	Scott	Entire Document
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U.S. PATENT APPLICATION PUBLICATIONS

Examiner Initial*	Cite No	Publication Number	Kind Code ¹	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	20020121679	A1	2002-10-29	Bazarjani et al.	Entire Document

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Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T ⁵
	1	USPTO Notice of Allowance for 10/238,966 (CD01250) dated 01/27/09; 4 pages	<input type="checkbox"/>
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	8	USPTO Notice of Allowance for 10/033,027 (CD00232) dated 03/31/09; 7 pages	<input type="checkbox"/>
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	10	USPTO Final Rejection for 10/033,027 (CD00232) dated 06/08/07; 8 pages	<input type="checkbox"/>

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11	USPTO Non-Final Rejection for 10/033,027 (CD00232) dated 12/21/06; 31 pages	<input type="checkbox"/>
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13	USPTO Non-Final Rejection for 10/033,027 (CD00232) dated 04/26/06; 26 pages	<input type="checkbox"/>
14	USPTO Final Rejection for 10/033,027 (CD00232) dated 10/31/05; 24 pages	<input type="checkbox"/>
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16	USPTO Non-Final Rejection for 10/033,027 (CD00232) dated 10/18/04; 17 pages	<input type="checkbox"/>
17	USPTO Non-Final Rejection for 10/001,478 (CD01213) dated 10/20/08; 18 pages	<input type="checkbox"/>
18	USPTO Final Rejection for 10/001,478 (CD01213) dated 06/04/08; 18 pages	<input type="checkbox"/>
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24	USPTO Final Rejection for 10/001,478 (CD01213) dated 10/24/05; 15 pages	<input type="checkbox"/>
25	USPTO Non-Final Rejection for 10/001,478 (CD01213) dated 05/16/05; 13 pages	<input type="checkbox"/>
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28	USPTO Notice of Allowance for 10/002,217 (CD01207) dated 06/06/08; 7 pages	<input type="checkbox"/>
29	USPTO Final Rejection for 10/002,217 (CD01207) dated 02/06/08; 10 pages	<input type="checkbox"/>
30	USPTO Non-Final Rejection for 10/002,217 (CD01207) dated 08/03/07; 10 pages	<input type="checkbox"/>
31	USPTO Final Rejection for 10/002,217 (CD01207) dated 03/07/07; 12 pages	<input type="checkbox"/>
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33	USPTO Non-Final Rejection for 10/002,217 (CD01207) dated 04/03/06; 12 pages	<input type="checkbox"/>
34	USPTO Final Rejection for 10/002,217 (CD01207) dated 11/17/05; 17 pages	<input type="checkbox"/>
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37	"An Analog PPL-Based Clock and Data Recovery Circuit with High Input Jitter Tolerance;" Sun, Reprinted from IEEE Journal of Solid-State Circuits, 1989; 4 pages	<input type="checkbox"/>
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40	Application Number 09/964,991 (CD00196): "A Novel Band-Gap Circuit for Providing an Accurate Reference Voltage Compensated for Process State, Process Variations and Temperature," Kutz et al., filed on 09/26/2001; 25 pages	<input type="checkbox"/>
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EXAMINER SIGNATURE

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	1	5982229	B1	1999-11-09	Wong et al.	Entire Document
	2	6535946	B1	2003-03-18	Bryant et al.	Entire Document
	3	4827401	B1	1989-05-02	Hrustich et al.	Entire Document
	4	5594734	B1	1997-01-14	Worsley et al.	Entire Document
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20	5331571	B1	1994-07-19	Aronoff et al.	Entire Document
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1	20020156998	A1	2002-10-24	Casselman, Steven	Entire Document
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	1	PCT/US99/09712	WO	A1	1999-02-25	Schutte	Entire Document	<input type="checkbox"/>
	2	PCT/US96/17305	WO	A1	1996-06-06	Schutte	Entire Document	<input type="checkbox"/>
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	1	USPTO Non-Final Rejection for 09/975,115 (CD00182) dated 10/09/08; 34 pages	<input type="checkbox"/>
	2	USPTO Final Rejection for 09/975,115 (CD00182) dated 05/12/08; 33 pages	<input type="checkbox"/>
	3	USPTO Non-Final Rejection for 09/975,115 (CD00182) dated 01/07/08; 30 pages	<input type="checkbox"/>

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34	Application Number 10/238,966 (CD01250M): "Method for Parameterizing a User Module," Perrin et al., filed on 09/09/2002; 41 pages	<input type="checkbox"/>
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37	USPTO Non-Final Rejection for 10/324,455 (CD98087C2) dated 08/21/2003; 4 pages	<input type="checkbox"/>
38	USPTO Notice of Allowance for 09/998,859 (CD01209M) dated 03/14/2005; 6 pages	<input type="checkbox"/>
39	USPTO Non-Final Rejection for 09/998,859 (CD01209M) dated 11/04/2004; 6 pages	<input type="checkbox"/>
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42	USPTO Non-Final Rejection for 09/998,859 (CD01209M) dated 05/15/2003; 6 pages	<input type="checkbox"/>
43	USPTO Notice of Allowance for 09/998,834 (CD01221M) dated 05/19/2005; 7 pages	<input type="checkbox"/>
44	USPTO Non-Final Rejection for 09/998,834 (CD01221M) dated 09/20/2004; 11 pages	<input type="checkbox"/>
45	USPTO Notice of Allowance for 10/113,065 (CD01215M) dated 04/06/2006; 8 pages	<input type="checkbox"/>
46	USPTO Final Rejection for 10/113,065 (CD01215M) dated 10/26/2005; 17 pages	<input type="checkbox"/>
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	1	Goodenough, F. "Analog Counterparts of FPGAS Ease System Design" Electronic Design, Penton Publishing, Cleveland, OH, US vol. 42, no. 21, 10/14/1994; 10 pages	<input type="checkbox"/>
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11	USPTO Application Number 09/953,423 (CD00187): "A Configurable Input/Output Interface for a Microcontroller," Warren Snyder, filed on 09/14/2001; 28 pages	<input type="checkbox"/>
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13	USPTO Application Number 09/929,891 (CD00226): "Programming Architecture for a Programmable Analog System," Mar et al., filed on 08/14/2001; 82 pages	<input type="checkbox"/>
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15	USPTO Non-Final Rejection for 09/994,601 (CD01170M) dated 11/14/2006; 15 pages	<input type="checkbox"/>
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Application Number	09989765
Filing Date	2001-11-19
First Named Inventor	Manfred Bartz
Art Unit	2176
Examiner Name	Maikhanh Nguyen
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26	USPTO Final Rejection for 09/989,771 (CD01182) dated 12/27/07; 15 pages	<input type="checkbox"/>
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33	USPTO Non-Final Rejection for 09/989,782 (CD01196M) dated 12/14/07; 8 pages	<input type="checkbox"/>
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	Examiner Name	Maikhanh Nguyen	
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¹ See Kind Codes of USPTO Patent Documents at www.USPTO.GOV or MPEP 901.04. ² Enter office that issued the document, by the two-letter code (WIPO Standard ST.3). ³ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁴ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁵ Applicant is to place a check mark here if English language translation is attached.

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number	09989765
	Filing Date	2001-11-19
	First Named Inventor	Manfred Bartz
	Art Unit	2176
	Examiner Name	Maikhanh Nguyen
	Attorney Docket Number	CD01194M

U.S. PATENTS

Examiner Initial*	Cite No	Patent Number	Kind Code†	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	7188063	B1	2007-03-06	Snyder, Warren	Entire Document
	2	7117485	B1	2006-10-03	Wilkinson et al.	Entire Document
	3	7017145	B1	2006-03-21	Taylor	Entire Document
	4	7024636	B1	2006-04-04	Weed	Entire Document
	5	7058921	B1	2006-06-06	Hwang et al.	Entire Document
	6	6282547	B1	2001-08-28	Hirsch	Entire Document
	7	6263339	B1	2001-07-17	Hirsch	Entire Document
	8	5983277	B1	1999-11-09	Heile, et al.	Entire Document

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9	6678877	B1	2004-01-13	Perry, et al.	Entire Document
10	6658633	B1	2003-12-02	Devins, et al.	Entire Document
11	6631508	B1	2003-10-07	Williams	Entire Document
12	6438738	B1	2002-08-20	Elayda	Entire Document
13	7206733	B1	2007-04-17	Nemecsek	Entire Document
14	6371878	B1	2004-04-16	Bowen	Entire Document
15	6278568	B1	2001-08-21	Cloke, et al.	Entire Document
16	6072803	B1	2000-06-06	Allmond, et al.	Entire Document
17	5841996	B1	1998-11-24	Nolan, et al.	Entire Document
18	5329471	B1	1994-07-12	Swoboda, et al.	Entire Document
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20	5574852	B1	1996-11-12	Bakker, et al.	Entire Document
21	5495593	B1	1996-02-27	Elmer, et al.	Entire Document
22	4670838	B1	1987-06-02	Kawata	Entire Document
23	4939637	B1	1990-07-03	Pawloski	Entire Document
24	4969087	B1	1990-11-06	Tanagawa, et al.	Entire Document
25	5313618	B1	1994-05-17	Pawloski	Entire Document
26	5371878	B1	1994-12-06	Coker	Entire Document

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Examiner Initial*	Cite No	Publication Number	Kind Code ¹	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	20050240917	A1	2005-10-27	Wu, Yuh-Cherng	Entire Document

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NON-PATENT LITERATURE DOCUMENTS

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	1	USPTO Notice of Allowance for 09/992,076 (CD01210M) dated 11/13/2008; 15 pages	<input type="checkbox"/>
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8	USPTO Non-Final Rejection for 09/992,076 (CD01210M) dated 06/01/2005; 20 pages	<input type="checkbox"/>
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26	USPTO Final Rejection for 10/001,477 (CD01203) dated 10/24/2005; 18 pages	<input type="checkbox"/>
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28	ITO, SERGIO AKIRA AND CARRO, LUIGI; "A Comparison of Microcontrollers Targeted to FPGA-Based Embedded Applications", September 2000, Proceedings of 13th Symposium on Integrated Circuits and Systems Design, 18-24 September 2000; 6 pages	<input type="checkbox"/>
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30	Monte Mar, Bert Sullam, Eric Blom; "An architecture for a configurable Mixed-signal device", 2003, IEEE Journal of Solid-State Circuits, Volume 3; 4 pages	<input type="checkbox"/>
31	ROBINSON, GORDON D; "Why 1149.1 (JTAG) Really Works", May 1994, Conference Proceedings Electro/94 International, 10-12 May 1994, Combined Volumes; 6 pages	<input type="checkbox"/>
32	USPTO Non-Final Rejection for 09/994,601 (CD01170) dated 10/04/2007; 20 pages	<input type="checkbox"/>
33	"PSoC designer: Integrated development environment, getting started 25-minute tutorial, version 1.0", Cypress Microsystems., Cypress Microsystems, Inc. CMS10006A, July 3, 2001; 25 pages	<input type="checkbox"/>
34	"PSoC technology complete changes 8-bit MCU system design", Cypress Microsystems, Inc. retrieved from <http://www.archive.org/web/20010219005250/http://cypressmicro.com-/t...>, Feb. 19, 2001; 21 pages	<input type="checkbox"/>
35	Specks et al., "A Mixed Digital-Analog 16B Microcontroller with 0.5MB Flash Memory, On-Chip Power Supply, Physical Network Interface, and 40V I/O for Automotive Single-Chip Mechatronics," IEEE, Feb. 9, 2000; 1 page	<input type="checkbox"/>
36	Hsieh et al., "Modeling Micro-Controller Peripherals for High-Level Co-Simulation and Synthesis," IEEE, 1997; 4 pages	<input type="checkbox"/>
37	USPTO Notice of Allowance for 09/989,777 (CD01208) dated 11/04/2008; 3 pages	<input type="checkbox"/>
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39	USPTO Notice of Allowance for 09/989,777 (CD01208) dated 07/07/2008; 23 pages	<input type="checkbox"/>
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42	USPTO Final Rejection for 09/989,777 (CD01208) dated 03/13/2007; 24 pages	<input type="checkbox"/>
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44	USPTO Non-Final Rejection for 09/989,777 (CD01208) dated 04/11/2006; 21 pages	<input type="checkbox"/>
45	USPTO Final Rejection for 09/989,777 (CD01208) dated 12/21/2005; 29 pages	<input type="checkbox"/>
46	USPTO Non-Final Rejection for 09/989,777 (CD01208) dated 07/05/2005; 36 pages	<input type="checkbox"/>
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48	USPTO Final Rejection for 09/994,601 (CD01170) dated 04/17/2008; 24 pages	<input type="checkbox"/>
49	USPTO Application Number 10/033,027 (CD00232M): "Microcontrollable Programmable System on a Chip," Warren Snyder; filed on 10/22/2001; 117 pages	<input type="checkbox"/>
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Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	6714066	B1	2004-03-30	Gorecki et al.	Entire Document
	2	6453175	B1	2002-09-17	Mizeli et al.	Entire Document
	3	6185127	B1	2001-02-06	Myers et al.	Entire Document
	4	6141376	B1	2000-10-31	Shaw	Entire Document
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9	6066961	B1	2000-05-23	Lee et al.	Entire Document
10	5396245	B1	1995-03-07	Rempfer	Entire Document
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12	5963075	B1	1999-10-05	Hiiragizawa	Entire Document
13	6150866	B1	2000-11-21	Eto et al.	Entire Document
14	5307381	B1	1994-04-26	Ahuja	Entire Document
15	6211715	B1	2001-04-03	Terauchi	Entire Document
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	20	6728900	B1	2004-04-27	Meli	Entire Document
	21	5544311	B1	1996-08-06	Harenberg et al.	Entire Document
	22	5664199	B1	1997-09-02	Kuwahara	Entire Document
	23	6175914	B1	2001-01-16	Mann	Entire Document
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31	6041406	B1	2000-03-21	Mann	Entire Document
32	5088822	B1	1992-02-18	Warren	Entire Document
33	5935266	B1	1999-08-10	Thurnhofer et al.	Entire Document
34	6463488	B1	2002-10-08	San Juan	Entire Document
35	5053949	B1	1991-10-01	Alison et al.	Entire Document
36	6026501	B1	2000-02-15	Hohl et al.	Entire Document
37	6567932	B1	2003-05-20	Edwards et al.	Entire Document
38	6591369	B1	2003-07-08	Edwards et al.	Entire Document
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42	5875293	B1	1999-02-23	Bell et al.	Entire Document
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44	6191603	B1	2001-02-20	Muradali et al.	Entire Document
45	6005814	B1	1999-12-21	Mulholland et al.	Entire Document
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49	6732068	B1	2004-05-04	Sample et al.	Entire Document
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	1	20040205695	A1	2004-10-14	Fletcher, David	Entire Document
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	5	USPTO Notice of Allowance for Application Number 09/953,423 (CD00187) dated 07/12/2004; 6 pages	<input type="checkbox"/>

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37	USPTO Notice of Allowance for Application Number 09/975,030 (CD00185) dated 02/06/2006; 7 pages	<input type="checkbox"/>
38	USPTO Non-Final Rejection for Application Number 09/975,030 (CD00185) dated 10/20/2005; 7 pages	<input type="checkbox"/>

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
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Application Number	09989765
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First Named Inventor	Manfred Bartz
Art Unit	2176
Examiner Name	Maikhanh Nguyen
Attorney Docket Number	CD01194M

39	USPTO Non-Final Rejection for Application Number 09/975,030 (CD00185) dated 03/29/2005; 13 pages	<input type="checkbox"/>
40	USPTO Notice of Allowance for Application Number 09/977,111 (CD00202) dated 09/28/2006; 6 pages	<input type="checkbox"/>
41	USPTO Notice of Allowance for Application Number 10/272,231 (CD00170C) dated 03/08/2004; 6 pages	<input type="checkbox"/>
42	USPTO Final Rejection for Application Number 10/272,231 (CD00170C) dated 11/05/2003; 5 pages	<input type="checkbox"/>
43	USPTO Non-Final Rejection for Application Number 10/272,231 (CD00170C) dated 07/14/2003; 6 pages	<input type="checkbox"/>
44	USPTO Notice of Allowance for Application Number 11/125,554 (CD00230C) dated 02/07/2008; 4 pages	<input type="checkbox"/>
45	USPTO Notice of Allowance for Application Number 11/125,554 (CD00230C) dated 04/24/2007; 7 pages	<input type="checkbox"/>
46	USPTO Non-Final Rejection for Application Number 11/125,554 (CD00230C) dated 12/11/2006; 9 pages	<input type="checkbox"/>
47	USPTO Notice of Allowance for Application Number 09/855,868 (CD00181) dated 04/25/2005; 6 pages	<input type="checkbox"/>
48	USPTO Non-Final Rejection for Application Number 09/855,868 (CD00181) dated 08/26/2004; 7 pages	<input type="checkbox"/>
49	USPTO Notice of Allowance for Application Number 09/887,923 (CD00229) dated 09/27/2004; 5 pages	<input type="checkbox"/>

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Attorney Docket Number	CD01194M

50	USPTO Non-Final Rejection for Application Number 09/887,923 (CD00229) dated 05/25/2004; 7 pages	<input type="checkbox"/>
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PTO/SB/08a (04-09)

Doc description: Information Disclosure Statement (IDS) Filed

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	Attorney Docket Number	CD01194M	

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Examiner Initial*	Cite No	Patent Number	Kind Code†	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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	9	6396302	B1	2002-05-28	New et al.	Entire Document
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32	6754765	B1	2004-06-22	Chang et al.	Entire Document
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	2	20020059543	A1	2002-05-16	Cheng et al.	Entire Document
	3	20010010083	A1	2001-07-26	Sato	Entire Document
	4	20020133794	A1	2002-09-19	Kanapathippillai et al.	Entire Document
	5	20020065646	A1	2002-05-30	Waldie et al.	Entire Document
	6	20020133771	A1	2002-09-19	Barnett	Entire Document

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	1	USPTO Notice of Allowance for Application Number 09/875,599 (CD00180) dated 10/17/2006; 4 pages	<input type="checkbox"/>
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	7	USPTO Non-Final Rejection for Application Number 09/875,599 (CD00180) dated 12/03/2004; 16 pages	<input type="checkbox"/>

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31	USPTO Notice of Allowance for Application Number 09/893,050 (CD00199) dated 07/05/2005; 6 pages	<input type="checkbox"/>
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45	USPTO Non-Final Rejection for Application Number 09/923,461 (CD00192) dated 07/16/2004; 6 pages	<input type="checkbox"/>
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47	USPTO Notice of Allowance for Application Number 09/929,891 (CD00226) dated 06/15/2005; 6 pages	<input type="checkbox"/>
48	USPTO Non-Final Rejection for Application Number 09/929,891 (CD00226) dated 09/13/2004; 6 pages	<input type="checkbox"/>
49	USPTO Notice of Allowance for Application Number 10/803,030 (CD00169C) dated 01/08/2007; 6 pages	<input type="checkbox"/>
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Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	4608502	B1	1986-08-26	Dijkmans et al.	Entire Document
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11	5455731	B1	1995-10-03	Parkinson	Entire Document
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20	5734272	B1	1998-03-31	Belot et al.	Entire Document
21	4999519	B1	1991-03-12	Kitsukawa et al.	Entire Document
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43	6246258	B1	2001-06-12	Lesea	Entire Document
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	1	USPTO Notice of Allowance for Application Number 10/011,214 (CD00191) dated 04/11/2005; 4 pages	<input type="checkbox"/>
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Application Number	09989765
Filing Date	2001-11-19
First Named Inventor	Manfred Bartz
Art Unit	2176
Examiner Name	Maikhanh Nguyen
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21	6718520	B1	2004-04-06	Merryman et al.	Entire Document
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32	6590517	B1	1993-07-08	Swanson	Entire Document
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	1	"New Object Domain R3 Beta Now Available (Build 134)!" March 13, 2001; < http://web.archive.org/web/200100331202605/www.objectdomain.com/domain30/index.html >; 2 pages	<input type="checkbox"/>
	2	"OMG XML Metadata Interchange (XMI) Specifications" 2000; 17 pages	<input type="checkbox"/>
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14	USPTO Requirement for Restriction/Election for Application Number 11/818,005 (CD01171C) dated 07/14/2009; 5 pages	<input type="checkbox"/>
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17	USPTO Advisory Action for Application Number 09/943,062 (CD00228) dated 09/25/2003; 3 pages	<input type="checkbox"/>
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32	USPTO Non-Final Rejection for Application Number 10/113,064 (CD01200M) dated 04/25/2005; 15 pages	<input type="checkbox"/>
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8	USPTO Advisory Action for Application Number 11/415,588 (CD05028) dated 01/14/2008; 3 pages	<input type="checkbox"/>
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30	USPTO Application Number 09/849,164 (CD01035): "Reduced Static Phase Error CMOS PLL Charge Pump," Jonathon Stiff, filed on 05/04/2001; 30 pages	<input type="checkbox"/>
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48	USPTO Application Number 09/404,891 (CD99054): "Method, Architecture and Circuitry for Controlling Pulse Width in a Phase and/or Frequency Detector," Scott et al., filed on 09/24/1999; 17 pages	<input type="checkbox"/>
49	USPTO Notice of Allowance for Application Number 10/226,911 (CD02112) dated 08/20/2004; 4 pages	<input type="checkbox"/>
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	Art Unit	2176	
	Examiner Name	Maikhanh Nguyen	
	Attorney Docket Number	CD01194M	

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	2	6560306	B1	2003-05-06	Duffy et al.	Entire Document
	3	6680632	B1	2004-01-20	Meyers et al.	Entire Document
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10	6597212	B1	2003-07-22	Wang et al.	Entire Document
11	5530444	B1	1996-06-25	Tice et al.	Entire Document
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13	7030688	B1	2006-04-18	Dosho et al.	Entire Document
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	1	USPTO Application Number 10/226,911 (CD02112): "Calibration of Integrated Circuit Time Constants," Gehring et al.; filed on 08/22/2002; 32 pages	<input type="checkbox"/>
	2	USPTO Notice of Allowance for Application Number 09/943,149 (CD01085) dated 01/12/2004; 7 pages	<input type="checkbox"/>
	3	USPTO Non-Final Rejection for Application Number 09/943,149 (CD01085) dated 08/28/2003; 9 pages	<input type="checkbox"/>
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6	USPTO Application Number 09/943,149 (CD01085): "Method for Phase Locking in a Phase Lock Loop," Moyal et al., filed on 08/30/2001; 21 pages	<input type="checkbox"/>
7	Durham et al., "Integrated Continuous-Time Balanced Filters for 16-bit DSP Interfaces," IEEE, 1993; 6 pages	<input type="checkbox"/>
8	Durham et al., "Circuit Architectures for High Linearity Monolithic Continuous-Time Filtering," IEEE, 1992; 7 pages	<input type="checkbox"/>
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10	USPTO Application Number 09/047,595 (CD97115): "Roving Range Control to Limit Receive PLL Frequency of Operation," Paul H. Scott, filed on 03/29/1998; 35 pages	<input type="checkbox"/>
11	USPTO Application Number 09/216,460 (CD98099): "Circuit and Method for Controlling an Output of a Ring Oscillator," Abugharbieh et al., filed on 12/18/1998; 21 pages	<input type="checkbox"/>
12	USPTO Application Number 09/471,914 (CD99064): "Reference-Free Clock Generator and Data Recovery PLL," Dalmia et al., filed on 12/23/1999; 32 pages	<input type="checkbox"/>
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14	USPTO Application Number 10/083,442 (CD01264): "Method/Architecture for a Low Gain PLL with Wide Frequency Range," Meyers et al., filed on 02/26/2002; 28 pages	<input type="checkbox"/>
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17	USPTO Application Number 09/608,753 (CD00018): "PLL Lockout Watchdog," Wilson et al., filed on 08/24/2004; 24 pages	<input type="checkbox"/>
18	USPTO Application Number 09/398,956 (CD99049): "Frequency Acquisition Rate Control in Phase Lock Loop Circuits," Moyal et al., filed on 09/17/1999; 35 pages	<input type="checkbox"/>
19	USPTO Application Number 09/747,262 (CD00085): "Linearized Digital Phase-Locked Loop," Williams et al., filed on 12/22/2000; 9 pages	<input type="checkbox"/>
20	USPTO Application Number 09/981,448 (CD01102): "Oscillator Tuning Method," Lane T. Hauck, filed on 10/17/2001; 28 pages	<input type="checkbox"/>
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22	USPTO Application Number 09/048,905 (CD95045): "Programmable Clock Generator," Mann et al., filed on 03/26/1998; 42 pages	<input type="checkbox"/>
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24	USPTO Notice of Allowance for Application Number 10/293,392 (CD02135) dated 03/10/2004; 6 pages	<input type="checkbox"/>
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28	USPTO Final Rejection for Application Number 10/288,003 (CD02108) dated 10/06/2004; 6 pages	<input type="checkbox"/>
29	USPTO Non-Final Rejection for Application Number 10/288,003 (CD02108) dated 04/07/2004; 9 pages	<input type="checkbox"/>
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8	USPTO Non-Final Rejection for Application Number 11/201,922 (CD04111) dated 10/15/2007; 10 pages	<input type="checkbox"/>
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19	USPTO Non-Final Rejection for Application Number 10/109,979 (CD01211M) dated 06/30/2005; 6 pages	<input type="checkbox"/>
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26	USPTO Application Number 09/989,819 (CD01181M): "System and method for creating a boot file utilizing a boot template," Ogami et al., filed on 11/19/2001; 43 pages	<input type="checkbox"/>
27	USPTO Notice of Allowance for Application Number 09/989,761 (CD01179M) dated 01/14/2005; 6 pages	<input type="checkbox"/>
28	USPTO Final Rejection for Application Number 09/989,761 (CD01179M) dated 08/26/2004; 7 pages	<input type="checkbox"/>
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30	USPTO Final Rejection for Application Number 09/989,761 (CD01179M) dated 10/03/2003; 7 pages	<input type="checkbox"/>
31	USPTO Non-Final Rejection for Application Number 09/989,761 (CD01179M) dated 04/18/2003; 5 pages	<input type="checkbox"/>
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33	Wang, et al. "Synthesizing Operating System Based Device Drivers in Embedded Systems," October 1-3, 2003; ACM; 8 pages	<input type="checkbox"/>
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38	PCT Written Opinion of the International Searching Authority for PCT/US2005/028793, filed 8/12/2005, mailed 11/19/2007; 7 pages	<input type="checkbox"/>
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41	Burogs et al., "Power Converter Analysis and Design using Matlab: A Transfer Function Approach," Proceedings of IEEE International Symposium on Industrial Electronics 1998, Vol. 2; 6 pages	<input type="checkbox"/>
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	1	6828824	B1	2004-12-07	Betz et al.	Entire Document
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37	6134181	B1	2000-10-17	Landry	Entire Document
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8	Written Opinion of the International Search Authority for International Application Number PCT/US08/60680 (CD2007130PCT) dated 08/15/2008; 4 pages	<input type="checkbox"/>
9	USPTO Notice of Allowance for Application Number 09/475,879 (CD99047) dated 10/22/2004; 7 pages	<input type="checkbox"/>
10	USPTO Advisory Action for Application Number 09/475,879 (CD99047) dated 03/04/2002; 3 pages	<input type="checkbox"/>
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16	USPTO Non-Final Rejection for Application Number 09/475,808 (CD99069) dated 06/06/2001; 8 pages	<input type="checkbox"/>
17	USPTO Notice of Allowance for Application Number 09/475,808 (CD99069) dated 11/06/2001; 8 pages	<input type="checkbox"/>
18	USPTO Application Number 10/137,497 (CD01197M): "Reconfigurable Testing System and Method," Pleis et al.; filed on 05/01/2002; 40 pages	<input type="checkbox"/>

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19	USPTO Non-Final Rejection for Application Number 10/137,497 (CD01197M) dated 11/05/2004; 17 pages	<input type="checkbox"/>
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27	USPTO Non-Final Rejection for Application Number 10/653,050 (CD01242C) dated 04/06/2004; 7 pages	<input type="checkbox"/>
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10	USPTO Non-Final Rejection for Application Number 11/337,272 (CD05191) dated 05/17/2007; 11 pages	<input type="checkbox"/>
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	Art Unit	2176	
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